



#### INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTTAYAM

ORGANIZES



4TH IEEE INTERNATIONAL CONFERENCE ON SIGNAL PROCESSING, INFORMATICS, COMMUNICATION AND ENERGY SYSTEMS 2024

**IEEE SPICES 2024** 

A FLAGSHIP CONFERENCE OF IEEE KERALA SECTION



20 - 22nd September 2024



IIIT Kottayam Valavoor, Pala Kottayam, Kerala

CONTACT US



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# Highlights of the conference

Technical paper sessions by academia and industry experts on the following tracks

- Signal Processing
- **Next Generation Communication Networks**
- **f** Intelligent Computing
- Advanced Analytics and AI Integration
- **Smart Energy Technologies**
- **Instrumentation for Smart Systems**
- **Cognitive Robotics**
- **Power Electronics and Smart Grids**

Tutorials and keynote lectures from academia and industry.

Panel discussions with experts from academia and industry.

Networking with 400+ researchers and professionals across the globe.









## Keynote & Tutorial speakers

Dr. Ganesh Neelakanta Iyer, National University of Singapore

**TOPIC:** Harnessing Signals, Transforming Data: Al's Impact on Cultural Heritage, **Education and Healthcare** 

Prof. Michael Sheng, Macquarie University Australia

**TOPIC:** Intelligent IoT Sensing for Aging Well: Research Activities and Future Directions

Dr. Sreejith Vidhyadharan Nair, University of North Dakota, USA

**TOPIC:** Advancing Airspace Safety and Security using UAV Risk analysis and Acoustic Counter-UAS

Dr. Hari Prabhat Gupta, IIT (BHU) Varanasi

**TOPIC:** Insights from Deploying and Measuring LoRa Technology in Smart Campus **Environments** 

Prof. Ponnurangam Kumaraguru, IIIT Hyderabad

**TOPIC : Responsible & Safe AI** 

Dr. Atif Iqbal, Qatar university, Qatar

**TOPIC:** Energy Transition for Net Zero-The Role of Renewable Energy and Electric **Vehicles** 

Prof. Dr. Marlene Harter, Offenburg University of Applied Sciences, Germany

**TOPIC:** Modern Radar Techniques for Automotive Applications

Dr. Arun Ashok, Co-Founder, Silizium Circuits Private Limited

**TOPIC:** Novel Integrated Computing Architectures as Alternatives to the Von Neumann **Paradigm** 









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